PATENT COOPERATION TREATY

PCT

Translation INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference			е	EOD EUDTHED A	CTION	See Form PCT/IPEA/416					
P2003,0256WO				FOR FURTHER ACTION							
International application No.				International filing date (day/month/year)		Priority date (day/month/year)					
PCT/EP2004/001615			515	19.02.2004		28.04.2003					
International Patent Classification (IPC) or national classification and IPC											
Applicant											
AUSTRIAMICROSYSTEMS AG											
1.	 This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36. 										
2.	This REPORT consists of a total of4				sheets, including this cover sheet.						
3.				NNEXES, comprising:							
	. [nnligant and	I to the International Pr	ragula total of	chaete as follows:					
	a. (sent to the applicant and to the International Bureau) a total of sheets, as follows: sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or										
	sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).										
	sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the Supplemental										
		Box.									
	b. (sent to the International Bureau only) a total of (indicate type and number of electronic carrier(s))										
						, containing a sequence listing and/or tables					
	related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see										
				trative Instructions).							
4.	_	port contains ind	ications relati	ing to the following iten	ns:						
ĺ	\boxtimes	Box No. I	Basis of the	e report							
		Box No. II	Priority								
		Box No. III	Non-establ	ishment of opinion with	regard to novelty, invent	tive step and industrial applicability					
		Box No. IV	Lack of un	ity of invention							
	Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement										
	Box No. VI Certain documents cited										
		Box No. VII	Certain def	ects in the international	application						
ļ	Box No. VIII Certain observations on the international application										
					Date of completion of th	nis report					
			-	!	or completion of the						
Name and mailing address of the IPEA/EP					Authorized officer						
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INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
PCT/EP2004/001615

Box No. I Basis of the report With regard to the language, this report is based on the international application in the language in which it was filed, unless otherwise indicated under this item. This report is based on translations from the original language into the following language which is the language of a translation furnished for the purposes of: international search (Rule 12.3 and 23.1(b)) publication of the international application (Rule 12.4) international preliminary examination (Rule 55.2 and/or 55.3) With regard to the elements of the international application, this report is based on (replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to the international application as originally filed/furnished the description: as originally filed/furnished pages received by this Authority on pages* _____ received by this Authority on _____ the claims: as originally filed/furnished as amended (together with any statement) under Article 19 received by this Authority on received by this Authority on the drawings: as originally filed/furnished sheets received by this Authority on sheets* received by this Authority on a sequence listing and/or any related table(s) - see Supplemental Box Relating to Sequence Listing. The amendments have resulted in the cancellation of: the description, pages the claims, nos. the drawings, sheets/figs the sequence listing (specify): any table(s) related to sequence listing (specify): This report has been established as if (some of) the amendments annexed to this report and listed below had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)). the description, pages the claims, nos. the drawings, sheets/figs the sequence listing (specify): any table(s) related to sequence listing (specify): If item 4 applies, some or all of those sheets may be marked "superseded."

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
PCT/EP2004/001615

Box	r No. V Reasoned statement citations and expla	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement				
1.	Statement					
	Novelty (N)	Claims	1-6	YES		
		Claims		NO		
	Inventive step (IS)	Claims	1-6	YES		
100		Claims		NO		
	Industrial applicability (IA)	Claims	1-6	YES		
		Claims		NO		
l						

- 2. Citations and explanations (Rule 70.7)
 - 1. This report makes reference to the following document:

D1, figure 2, which is regarded as the closest prior art, discloses a master-slave flip-flop switch arrangement in emitter coupled logic, in which arrangement switches Q1, Q4, Q7 and Q10, which are transversely controlled by the clock signal T, T, short circuit the associated ECL differential amplifier stages in a manner dependent on the state of the clock signal. The circuit according to D1 therefore likewise offers the advantage found in the application that a low supply voltage is possible because the transistors controlled by the clock signal do not switch the ECL current sources among ECL differential amplifiers.

The problem to be solved with respect to D1 is that of further reducing the amount of electricity required by the circuit (application, page 3, third paragraph).

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

According to the application, this can be done successfully if the four differential amplifiers of the two claimed flip flops have only two sources of current, whereby the initial stage of the one flip flop shares a current source with the cross-coupled stage of the other flip flop.

This feature is neither derivable from nor rendered obvious by the available prior art.

Therefore, the subject matter of claim 1 is novel (PCT Article 33(2)) and involves an inventive step (PCT Article 33(3)).

Claims 2 to 6 are dependent on claim 1 and therefore likewise meet the PCT requirements for novelty and inventive step.

Industrial applicability is established without a doubt.